

Manufacturer : Anshan Keifat Electronic Ceramic Technical Co.,Ltd.

No:

## Approval Sheet for Product Specification

**Customer:**

**Product: Epoxy Molding SMD Y2-cap**

**PART No.:**

**Mfr. P/N:**

**Date:**        年    月    日

Manufacturer		Customer Confirm	
Prepared by	薛志豪	合 格 OK <input type="checkbox"/>	
		不合格 NG <input type="checkbox"/>	
Checked by	于金龙	Checked by	
Approved by	范垂旭	Approved by	

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## Revision History

[illegible]



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<div>■ Features</div> <div><div>1. We design capacitors much more compact in thickness than traditional radial Type, having reduced the thickness to 2.5mm height.</div><div>2. Operating temperature range guaranteed up to 125 degrees C.</div><div>3. Dielectric strength: AC2500V</div><div>4. Class X1/Y2 capacitors certified by UL/ VDE /ENEC</div><div>5. Coated with flame - retardant epoxy resin (conforming to UL94V - 0 standard). We recommend a halogen - free &amp; beryllium free product* as our standard item.</div><div>* Cl =900ppm max., Br=900ppm max. and Cl+Br=1500ppm max.</div><div>6. Taping available for automatic SMT reflow.</div><div>7. AC250V Rated Voltage item is available.</div><div>8. This one is MSL 3 product. So, in order to avoid the absorption of moisture, capacitors are packed in moisture-proof envelope.</div><div>Store the capacitors in the following conditions at all times, and use within 6 months after delivered.</div><div>Temperature:10 to 30℃</div><div>Humidity: 60%max.</div><div>Solder the enclosed capacitors within 168 hours after opening the moisture-proof package. After opening, store the capacitors in moisture-proof package with a desiccant and HIC card and keep the above condition.</div><div>In case the storage period has been exceeded 6 months or the indicator color of a enclosed HIC card has changed when the package has been opened, perform baking (60℃x168hr)before soldering.</div><div>9. When the product is unpacked, the exposure time exceeds Floor time, the temperature and humidity around the product exceed the requirement. Reference condition for drying mounted or unmounted SMD packages (user bake: Floor life begins at time=0 after bake)</div></div> <table><tr><td rowspan="2">Level</td><td colspan="2">Bake@40℃ ≤5%RH</td></tr><tr><td>Saturated@30℃/85%RH</td><td>At limit of Floor life+72hr@30℃/60RH</td></tr><tr><td>3</td><td>79days</td><td>67days</td></tr></table> <div>■ Application</div> <div><div>1. Ideal for use as X/Y capacitors for AC line filters and primary-secondary coupling on switching power supplies and AC adapters.</div><div>2. Ideal for use on D-A isolation and noise absorption for DAA modems without transformers.</div><div>3. Mounting Capacitor on both sides of PCB, increasing the space utilization ratio.</div></div> <div>■ Part Number Designation</div> <div>CT7 -250VAC - Y2 - B - 101 K I S</div> <div><div>①</div><div>②</div><div>③</div><div>④</div><div>⑤</div><div>⑥</div><div>⑦</div><div>⑧</div></div>				Level	Bake@40℃ ≤5%RH		Saturated@30℃/85%RH	At limit of Floor life+72hr@30℃/60RH	3	79days	67days
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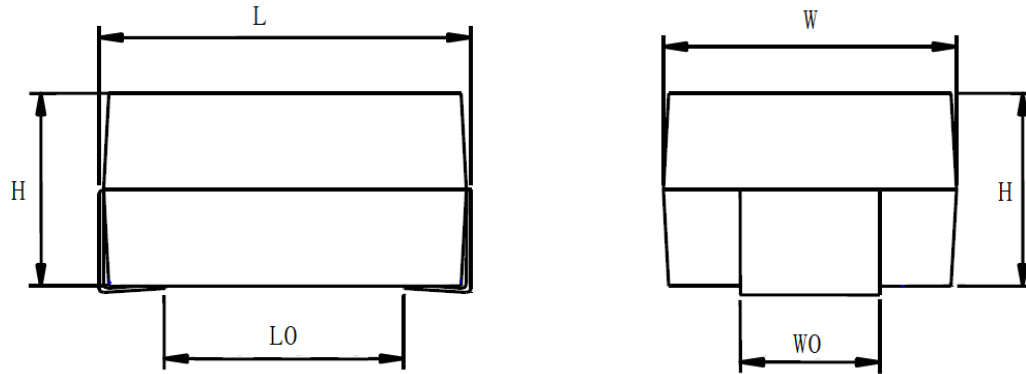
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<b>①Type</b>																					
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<b>②Rated Voltage</b>																					
<table border="1"><thead><tr><th>Code</th><th>Rated Vol. (AC)</th></tr></thead><tbody><tr><td>250VAC</td><td>250V</td></tr></tbody></table>				Code	Rated Vol. (AC)	250VAC	250V														
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250VAC	250V																				
<b>③Class Code</b>																					
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<b>④Temperature Characteristic</b>																					
<table border="1"><thead><tr><th>GB</th><th>EIA</th><th>Temp. range</th><th>Cap. Change</th></tr></thead><tbody><tr><td colspan="2">SL</td><td>+25~+85℃</td><td>+350~-1000ppm/℃</td></tr><tr><td>B</td><td>Y5P</td><td>-25~+85℃</td><td>±10%</td></tr><tr><td>E</td><td>Y5U</td><td>-25~+85℃</td><td>-56%~+22%</td></tr></tbody></table>				GB	EIA	Temp. range	Cap. Change	SL		+25~+85℃	+350~-1000ppm/℃	B	Y5P	-25~+85℃	±10%	E	Y5U	-25~+85℃	-56%~+22%		
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SL		+25~+85℃	+350~-1000ppm/℃																		
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<b>⑤Capacitance</b>																					
<table border="1"><thead><tr><th>Code</th><th>Capacitance</th></tr></thead><tbody><tr><td>101</td><td>100 pF</td></tr><tr><td>102</td><td>1000 pF</td></tr></tbody></table>				Code	Capacitance	101	100 pF	102	1000 pF												
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101	100 pF																				
102	1000 pF																				
<b>⑥Tolerance</b>																					
<table border="1"><thead><tr><th>Code</th><th>Tolerance</th></tr></thead><tbody><tr><td>K</td><td>±10%</td></tr><tr><td>M</td><td>±20%</td></tr></tbody></table>				Code	Tolerance	K	±10%	M	±20%												
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<b>⑦ Shaping method</b>																					
<table border="1"><thead><tr><th>Code</th><th>Description</th></tr></thead><tbody><tr><td>I</td><td>In-kink</td></tr><tr><td>O</td><td>Out-kink</td></tr></tbody></table>				Code	Description	I	In-kink	O	Out-kink												
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I	In-kink																				
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<b>⑧Lead Shape</b>																					
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	CT7-250VAC-Y2-B-471K IS/OS																				
	CT7-250VAC-Y2-E-102M IS/OS																				



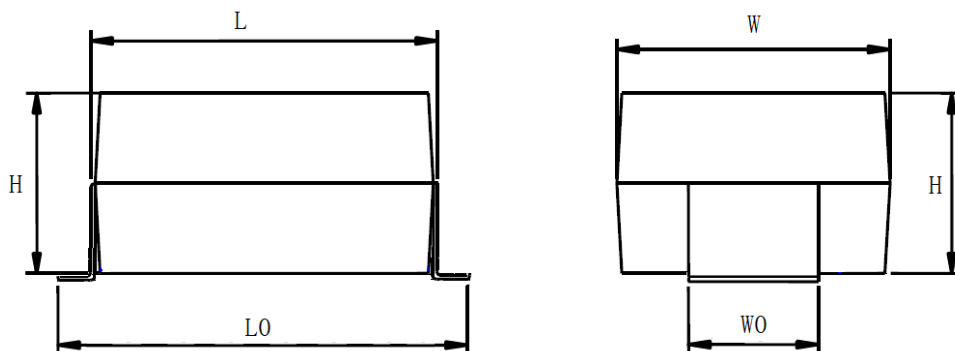
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■ Appearance and Dimension (Unit: mm)

IS Series

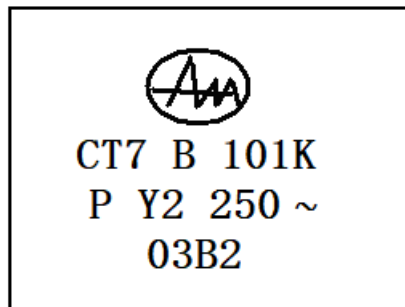


OS Series



Shaping method	L	W	H	L0	W0
IS	$6.2 \pm 0.3$	$5.2 \pm 0.3$	$2.40 \pm 0.15$	$4.2 \pm 0.3$	$2.5 \pm 0.2$
OS	$6.2 \pm 0.3$	$5.2 \pm 0.3$	$2.40 \pm 0.15$	$8.4 \pm 0.3$	$2.5 \pm 0.2$

■ Marking



	Manufacturer's Marking
CT7	Type Designation
B	Temperature Characteristic
101	Nominal Capacitance
K	Capacitance Tolerance
P	Pb solder product
Y2	Class code
250~	Rated Voltage Mark
03B2	Manufactured Date Code (0: Year, 3: Month, B: Date, 1: Sequence code)



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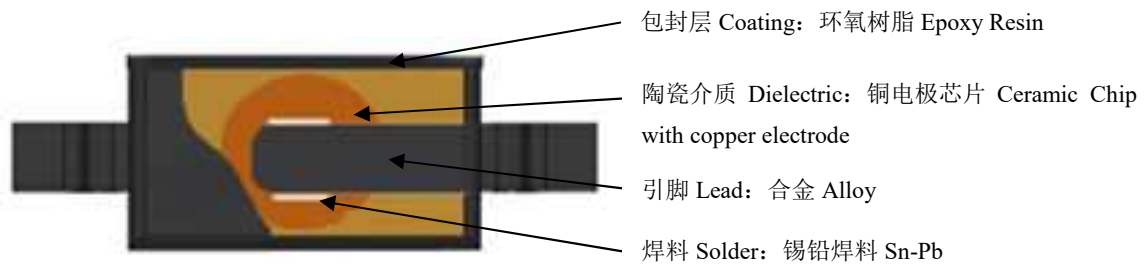
■ Safety Certification

No	Certificate authority	Certificate No	Rated voltage
1	ENEC	40043423	400VAC-Y1/X1,250VAC-Y1/Y2/X1
2	VDE	40043423	400VAC-Y1/X1,250VAC-Y1/Y2/X1
3	UL	E232980	400VAC-Y1/X1,250VAC-Y1/Y2/X1

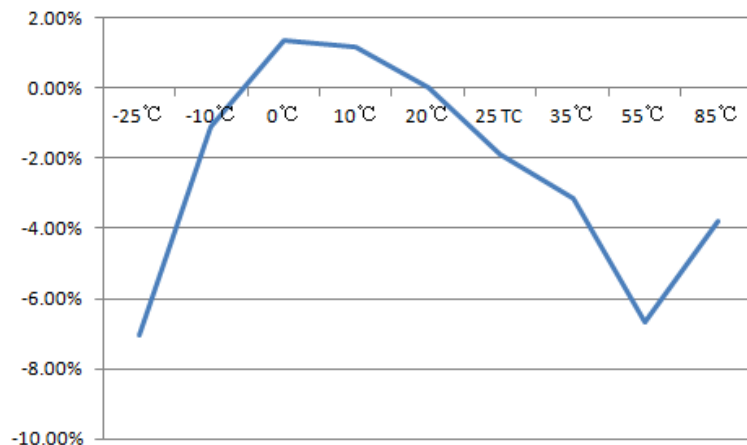
■ Exemption Clause

2010/571/EU 7(a): Lead in high melting temperature type solders (i.e. lead- based alloys containing 85 % by weight or more lead)

■ Structure



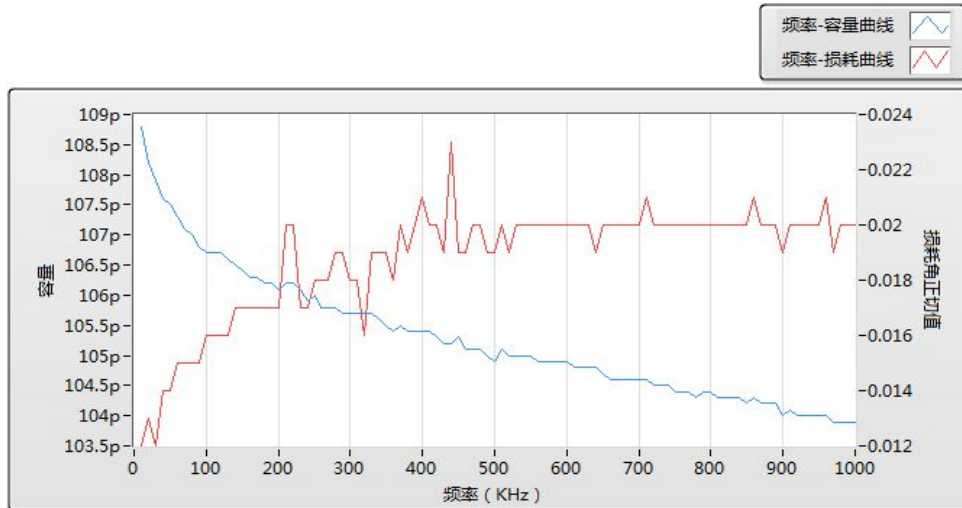
■ Temperature Characteristic Curve





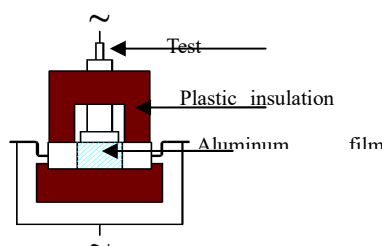
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■ Cap. & D.F.—Fre. Curve

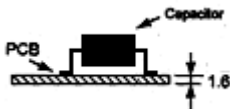
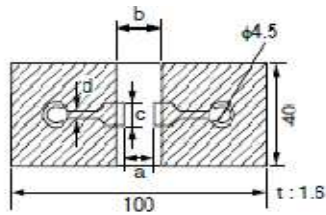
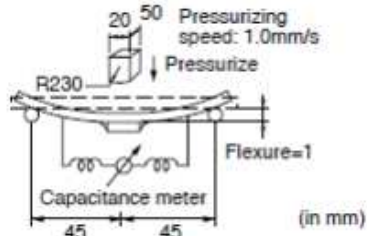




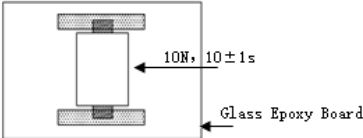
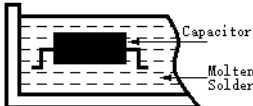


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■ Specification and Test Method					
Item		Specifications		Test Method	
1 Operating Temp. Range		-40℃~+125℃			
2 Appearance		No defects or abnormalities		Visual inspection	
3 Dimensions		Within the specified dimensions		Dimension be measured by caliper	
4 Marking		To be easily legible		The capacitor should be visually inspected.	
5 Capacitance		In specified tolerance		Temp. 20℃±2℃ , Vol. AC 5Vrms Max. Freq. SL: 1±0.1MHz , B、E:1±0.1KHz, The capacitance, dissipation factor should be measured at 20℃ with 1±0.1KHz (char. SL: 1±0.1MHz) and AC 5Vrms Max.	
6 Dissipation Factor(D.F.)/Q		Char.	Specifications		
		SL	≤0.15%		
		B、E	≤2.5%		
7 Insulation Resistance (I.R.)		10000MΩ min		The insulation resistance should be measured with DC 500±50V within 60±5 sec. of charging. The voltage should be applied to the capacitor through a resistor of 1MΩ.	
8 Dielectric Strength	Between Lead Wires	No failure		The capacitor should not be damaged when the test voltages from Table 1 are applied between the lead wires for 60 sec.	
		Char.	Leakage current		
		SL、B、E	1.0mA max.		
	Body Insulation	No failure			
		First, the terminals of the capacitor should be connected together, and the capacitor should be wrapped closely with an aluminum film around the body of the capacitor to a distance about 2 to 3mm from each terminal. Then, put the capacitor into the testing jig as shown in below figure. Finally, apply a voltage of Table 2 for 60 sec.			
					

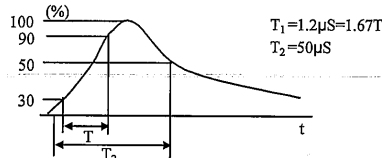


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Item		Specifications		Test Method														
9 Temperature Characteristic		Char.	Capacitance Change		The capacitance should be measured at each step as below table. <table><tr><td>Step</td><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td></tr><tr><td>Temp.( °C)</td><td>20±2</td><td>-25±2</td><td>20±2</td><td>85±2</td><td>20±2</td></tr></table>		Step	1	2	3	4	5	Temp.( °C)	20±2	-25±2	20±2	85±2	20±2
		Step	1	2			3	4	5									
		Temp.( °C)	20±2	-25±2			20±2	85±2	20±2									
		SL	+350~-1000ppm/°C (+20°C~+85°C)															
B	±10%																	
E	-56%~+22%																	
10 Vibration Resistance	Appearance	No marked defect		Solder the capacitor to the test jig (glass epoxy board). The capacitor should be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 to 55Hz.The frequency range, from 10 to 55Hz and return to 10Hz, should be traversed in approximately 1min.This motion should be applied for a period of 2hrs.in each of 3 mutually perpendicular directions (total of 6hrs.). 														
11 Solder ability of leads		75% of the terminations are to be soldered evenly and continuously.		Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in solder solution for 2±0.5 sec. Immersing speed: 25±2.5mm/s Temp. of solder: 245±5°C Lead Free Solder (Sn-3.0Ag-0.5Cu)														
12 Deflection		No marked defect 		Solder the capacitor to the test jig(glass epoxy board)shown in Fig.1 Then apply a force in the direction shown in Fig.1. The soldering should be done using reflow method and should be conducted with care so that the soldering is uniform a free of defects such as heat shock.  Fig.1														
		<table><tr><th colspan="4">Dimension(mm)</th></tr><tr><td>a</td><td>b</td><td>c</td><td>d</td></tr><tr><td>9.6</td><td>11.7</td><td>2.7</td><td>1.0</td></tr></table>				Dimension(mm)				a	b	c	d	9.6	11.7	2.7	1.0	
		Dimension(mm)																
a	b	c	d															
9.6	11.7	2.7	1.0															

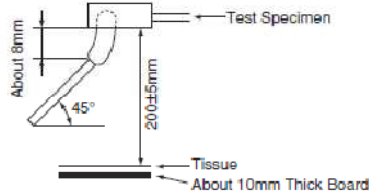
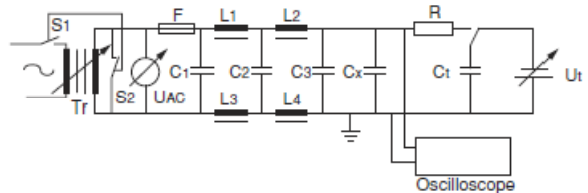
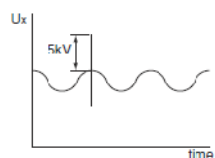


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Item		Specifications		Test Method												
13 Adhesive Strength of Termination		No removal of the terminations or other defect should occur.		<p>Solder the capacitor to the test jig(glass epoxy board)shown in Fig.2</p> <p>Then apply 10N force in the direction of the arrow. The soldering should be done using reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock.</p> <div></div> <p>Fig. 2</p>												
14 Resistance to Soldering Heat	Appearance	No marked defect		<p>Preheat the capacitor as in table. Immerse the capacitor in solder solution at 260±5℃for 10±1 sec. Let sit at room condition for 24±2 hrs., then measured.</p> <p>Immersing speed: 25±2.5mm/s</p> <p>Pretreatment for Y5P、Y5U char.</p> <p>Perform a heat treatment at 150<sup>+0</sup><sub>-10</sub> °C for 60±5 min. and then let sit for 24±2 hrs. at room condition.</p> <p>Preheating</p> <table><tr><th>Step</th><th>Temperature</th><th>Time</th></tr><tr><td>1</td><td>100 to 120℃</td><td>1 min.</td></tr><tr><td>2</td><td>170 to 200℃</td><td>1 min.</td></tr></table> <div></div>				Step	Temperature	Time	1	100 to 120℃	1 min.	2	170 to 200℃	1 min.
	Step	Temperature	Time													
	1	100 to 120℃	1 min.													
	2	170 to 200℃	1 min.													
	Capacitance Change	Char.	Capacitance Change													
SL	±5% or ±0.5pF (whichever is larger)															
B	±10%															
E	±15%															
I.R.	1000MΩ min															
Dielectric Strength	Per Item 8.															
15 稳态湿热 Humidity (under Steady State)	Appearance	No marked defect		<p>Before this test, the test shown in the following is performed.</p> <ul style="list-style-type: none"><li>Item 12 Deflection</li><li>Item 13 Adhesive Strength of Termination (applied force is 5N)</li></ul> <p>Set the capacitor for 500<sup>+24</sup><sub>-0</sub> hrs. at 40±2℃ in 90 to 95% relative humidity .</p> <p>Pretreatment for Y5P、Y5U char.</p> <p>Perform a heat treatment at 150<sup>+0</sup><sub>-10</sub> °C for 60±5 min. and then let sit for 24±2 hrs. at room condition.</p> <p>Post-treatment: The capacitor should be stored for 1 to 2 hours at room condition.</p>												
	Capacitance Change	Char.	Capacitance Change													
	SL	±5% or ±0.5pF (whichever is larger)														
	B	±10%														
	E	±15%														
	D.F./Q	Char.	Specifications													
	SL	≤0.3%														
B、E	≤5.0%															
I.R.	3000MΩ min															
Dielectric Strength	Per Item 8.															



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Item	Specifications		Test Method	
16 Humidity Loading	Appearance	No marked defect	<p>Before this test, the test shown in the following is performed.</p> <ul style="list-style-type: none"> <li>Item 12 Deflection</li> <li>Item 13 Adhesive Strength of Termination (applied force is 5N)</li> </ul> <p>Apply the rated voltage for <math>500^{+24}_{-0}</math> hrs. at <math>40 \pm 2^\circ\text{C}</math> in 90% to 95% relative humidity .</p> <p>Pretreatment for Y5P、Y5U char.</p> <p>Perform a heat treatment at <math>150^{+0}_{-10}^\circ\text{C}</math> for <math>60 \pm 5</math> min. and then let sit for <math>24 \pm 2</math> hrs. at room condition.</p> <p>Post-treatment:</p> <p>Capacitor should be stored for <math>24 \pm 2</math> hrs. at room condition</p>	
	Capacitance Change	Char.		
		SL		
		B		
		E		
	D.F./Q	Char.		
		SL		
		B、E		
17 Life	I.R.	3000 M $\Omega$ min	<p>Before this test, the test shown in the following is performed.</p> <ul style="list-style-type: none"> <li>Item 12 Deflection</li> <li>Item 13 Adhesive Strength of Termination (applied force is 5N)</li> </ul> <p>Impulse voltage</p> <p>Each individual capacitor should be subjected to a 5KV (X1Y2) / 8KV (X1Y1) impulse for three times. Then the capacitors are applied to life test.</p>  <p>Apply a voltage of 170% of rated voltage except that once each hour the voltage is increased to AC1000V (r.m.s.) for 0.1sec for 1000 hrs., at <math>125^{+2}_{-0}^\circ\text{C}</math> and relative humidity of 50% max.</p> <p>Pretreatment for Y5P、Y5U char.</p> <p>Perform a heat treatment at <math>150^{+0}_{-10}^\circ\text{C}</math> for <math>60 \pm 5</math> min. and then let sit for <math>24 \pm 2</math> hrs. at room condition.</p> <p>Post-treatment:</p> <p>Capacitors should be stored for <math>24 \pm 2</math> hrs at room condition.</p>	
	Capacitance Change	Char.		
	Capacitance Change	SL		
		B、E		
	I.R.	3000M $\Omega$ min		
	Dielectric Strength	Per Item 8.		



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Item	Specifications	Test Method	
18 Passive Flammability	<p>The burning time should not exceed 30 sec.</p> <p>The tissue paper should not ignite.</p>	<p>The capacitor under test should be held in the flame in the position that best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame: 30 sec.</p> <p>Length of flame : <math>12 \pm 1\text{mm}</math></p> <p>Gas burner: Length 35mm min. Inside Dia. <math>0.5 \pm 0.1\text{mm}</math> Outside Dia. 0.9mm max.</p> <p>Gas: Butane gas Purity 95% min.</p> 	
19 Active Flammability	<p>The cheesecloth should not be on fire.</p>	<p>The capacitor should be individually wrapped in at least one but not more than two complete layers of cheesecloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The <math>U_{AC}</math> should be maintained for 2 min. after the last discharge.</p>  <p> <math>C_{1,2} : 1\mu\text{F} \pm 10\%</math>    <math>C_3 : 0.033\mu\text{F} \pm 5\%</math> 10kV  L1 to 4 : 1.5mH <math>\pm 20\%</math> 16A Rod core choke  <math>C_t : 3\mu\text{F} \pm 5\%</math> 10kV    <math>R : 100\Omega \pm 2\%</math>  Cx : Capacitor under test    <math>U_{AC} : U_R \pm 5\%</math>  F : Fuse, Rated 10A    <math>U_R</math> : Rated Voltage  <math>U_t</math> : Voltage applied to <math>C_t</math> </p> 	



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Item		Specifications		Test Method			
21 Temperature and immersion Cycle	Appearance	No marked defect		The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles.  < Temperature Cycle >  			



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#### ■ Package

Paste product label and moisture-sensitive warning label on the taping reel, then, take the reel, HIC card and desiccant(30g) into the package bag, vacuum sealed.

#### Product Label

QUALIFICATION CERTIFICATE					
1	CODE		TEST BY	QC05	6
2	ITEM	SMD Y2-cap			
3	SPEC	CT7-250VAC-Y2-E-102M SMD P			
4	Q'TY	50 PCS	DATE	2019.06.28	7
5	REM	Pb Solder	LOT	SAMPLE	8
ANSHAN KEI FAT Electronic Ceramic Technical Co.,Ltd.					

No.	Description	No.	Description
1	Code Number	5	Remark
2	ITEM	6	Check
3	SPEC	7	Produce Date
4	Quantity	8	Batch

#### Package style and moisture-sensitive warning label Label



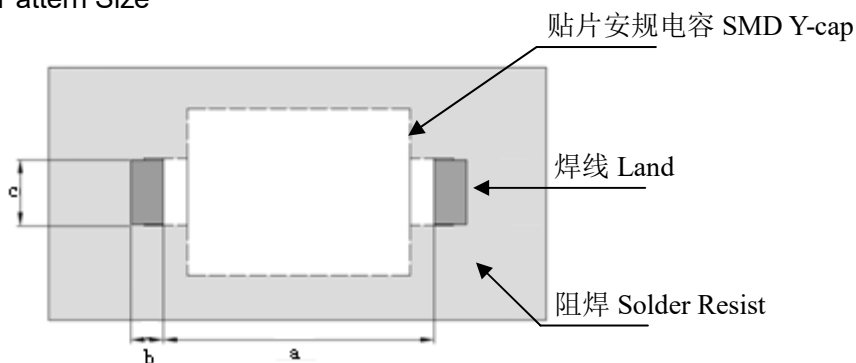
警告：湿度敏感产品 Caution: moisture-sensitive devices		Level 3
储存条件 Storage Condition	Temperature: 10 to 30℃ Humidity: 60%max.	
使用期限 Using Period	6个月 6months	
打开包装后使用条件 Using Condition after opening	打开包装后室内环境30℃/60%RH, 168小时内回流焊接 未使用产品附干燥剂和HIC卡密封包装储存 Mounted & Reflow within 168hr of factory condition ≤30℃/60%RH Stored in moisture-proof package with a desiccant and HIC card	
后处理 Post-treatment	如果超过6个月的储存期, 或包装打开后随附的HIC卡的指示颜色发生变化, 则应在焊接前进行烘烤 (60℃x168hr) In case the storage period has been exceeded 6 months or the color of HIC card has changed, perform baking (60℃x168hr) before soldering.	



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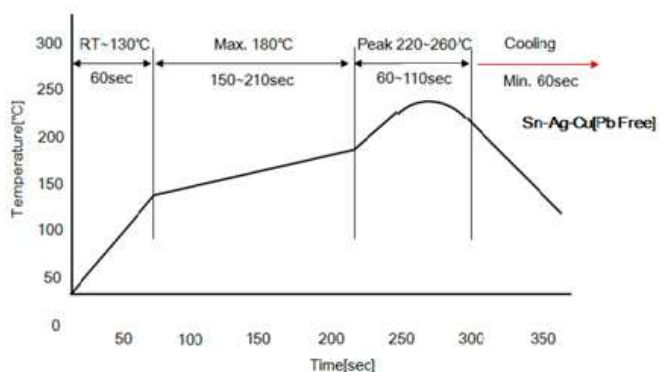
## ■ Recommended Soldering Condition

### 1. Soldering Land Pattern Size



Shaping method	a	b	c
IS	$4.2 \pm 0.3\text{mm}$	$0.9 \pm 0.1\text{mm}$	$2.6 \pm 0.1\text{mm}$
OS	$8.4 \pm 0.3\text{mm}$	$0.9 \pm 0.1\text{mm}$	$2.6 \pm 0.1\text{mm}$

### 2. Reflow Soldering Temperature Profile



Zone	Temp. range (°C)	Time(sec)	Remark
a	RT~130	60	Solder: Sn-Ag-Cu Peak time: less than 10 sec
b	180 max	150~210	
c	220~260(260 max)	60~110	
d	220~RT	60 min	

Solder ability of tin plating termination pins might be deteriorated when a low temperature soldering profile where the peak solder temperature is below the melting point of tin is used. Please confirm the solder ability of tin plated termination pins before use

The maximum temperature in the air outlet and the space of Reflow soldering is 280°C max., if the temperature exceed, it maybe a failure occur. Our company will not be held responsible for any adverse effects caused by over temperature using

## ■ Caution (Rating)

### 1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the V p-p value of the applied voltage or the Vo-p that contains DC bias within the rated voltage range.





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When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement					

## 2. Operating Temperature and Self-generated Heat (Apply to B/E Char.)

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may have self-generated heat due to dielectric loss. Applied voltage load should be such that self-generated heat is within 20°C under the condition where the capacitor is subjected to an atmospheric temperature of 25°C. When measuring, use a thermocouple of small thermal capacity-K of  $\Phi 0.1\text{mm}$  under conditions where the capacitor is not affected by radiant heat from other components or wind from surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

## 3. Test Condition for Withstanding Voltage

### (1) Test Equipment

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60Hz sine wave.

If the distorted sine wave or overload exceeding the specified voltage value is applied, a defect may be caused.

### (2) Voltage Applied Method

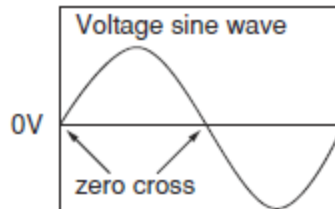
When the withstanding voltage is applied, the capacitor's lead or terminal should be firmly connected to the output of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the zero cross.\* At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the output of the withstanding voltage test equipment. If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may rise, and therefore, a defect may be caused.



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\*ZERO CROSS is the point where voltage sine wave passes 0V. See the figure at below.



#### 4. Fail-Safe

When the capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure could result in an electric shock, fire or fuming.

**FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.**

#### Caution (Storage and Operating Condition)

The insulating Epoxy molded capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect produce quality by testing the performance of a cleaned, bonded or molded product in the intended equipment.

This one is MSL 3 product. So, in order to avoid the absorption of moisture, capacitors are packed in moisture-proof envelope.

Store the capacitors in the following conditions at all times, and use within 6 months after delivered.

Temperature: 10 to 30°C

Humidity: 60%max.

Solder the enclosed capacitors within 168 hours after opening the moisture-proof package. After opening, store the capacitors in moisture-proof package with a desiccant and HIC card and keep the above condition.

In case the storage period has been exceeded 6 months or the indicator color of a enclosed HIC card has changed when the package has been opened, perform baking (60°C×168hr) before soldering.

When the product is unpacked, the exposure time exceeds Floor time, the temperature and humidity around the product exceed the requirement. Reference condition for drying mounted or unmounted SMD packages (user bake: Floor life begins at time=0 after bake)



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Level	Bake@40°C ≤5%RH			
	Saturated@30°C/85%RH	At limit of Floor life+72hr@30°C/60RH		
3	79days	67days		
FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOCT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.				
Caution (Soldering, Mounting and Handing)				
1. Vibration and Impact				
Do not expose a capacitor or its pins to excessive shock or vibration during use.				
Excessive shock or vibration may cause fatigue destruction of lead wires mounted on the circuit board.				
Please take measures to hold a capacitor on the circuit boards by adhesive, molding resin or another coating.				
Please confirm there is no influence of holding measures on the product with the intended equipment.				
2. Soldering				
When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specifications of the capacitor.				
Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.				
Soldering the capacitor with a soldering iron should be performed in the following conditions.				
Temperature of iron-tip: 400 degrees C. max.				
Soldering iron wattage: 50W max.				
Soldering time: 3.5 sec. max.				
3. Bonding, Resin Molding and Coating				
Before bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.				
In case the amount of applications, dryness/hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.				
The variation in thickness of adhesive, molding resin or coating may cause outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.				
4. Treatment after Bonding, Resin Molding and Coating				
When the outer coating is hot (over 100 degrees C.) after soldering, it becomes soft and fragile. Therefore, please be careful not to give it mechanical stress.				
FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOCT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.				



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<b>Notice (Soldering and Mounting)</b>  Cleaning (ultrasonic cleaning)  To perform ultrasonic cleaning, observe the following conditions.  Rinse bath capacity: Output of 20 watts per liter or less. Rinsing time: 5 min. max.  Do not vibrate the PCB/PWB directly. Excessive ultrasonic cleaning may lead to fatigue destruction of the pins.  <b>Notice (Rating)</b>  1. Capacitance Change of Capacitors  (1) For CH/SL/DL char.  Capacitance might change a little depending on a surrounding temperature or an applied voltage.  Please contact us if you use a strict constant time circuit.  (2) For B/E char.  Capacitors have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor is left on for a long time. Moreover, capacitance might change greatly depending on the surrounding temperature or an applied voltage. Therefore, it is not likely to be suitable for use in a constant time circuit.  Please contact us if you need detailed information.  2. Performance Check by Equipment  Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.  Generally speaking, CLASS 2 (B/E char.) ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance, so the capacitance value may change depending on the operating condition in the equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in the capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.  Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.		